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APPLICANTS

Goro Nakatani, Kyoto, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 10/205,867 07/24/2002 PAT 6,794,732

** FOREIGN APPLICATIONS *****

JAPAN 2001-224769 07/25/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/22/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>[Initials]</i> Initials			

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TITLE

Semiconductor device and method of manufacturing the same

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